

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO. 40296-0013

In re patent application of:
Sang Sic YOON

Serial No.: Unassigned

Filed: Concurrently herewith

For: DIGITAL CONTROL LOGIC CIRCUIT HAVING A CHARACTERISTIC OF TIME
HYSTERESIS

PRELIMINARY AMENDMENT

Commissioner of Patents
Washington, DC 20231

Sir:

Preliminary to examination please amend the above-identified Application as set forth in the Amendments below and in the attached Marked Copy.

IN THE CLAIMS:

Please amend and add claims set forth below.

5. (Amended) The circuit according to claim 3, wherein the second delay time is more than two times longer than the first delay time.

Please add the following new claims:

8. The circuit according to claim 4, wherein the second delay time is more than two times longer than the first delay time.